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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appin, of:

Karlheinz Müller et al.

Appln. No.:

10/526,818

Filed:

March 3, 2005

For:

METHOD FOR FABRICATING

AN INTEGRATED PIN DIODE AND ASSOCIATED CIRCUIT

ARRANGEMENT

Attorney Docket No: 10808-222

Examiner: Chandra P. Chaudhari

Art Unit: 2891

REPLY UNDER 37 C.F.R. §1.111

In response to the Office Action mailed April 17, 2007, Applicants respectfully submits this paper comprising the following parts:

> 1. Amendments to the Abstract

Page 2

11. Remarks Page 3

Applicants note that the Transmittal to which this paper is attached includes a Certificate of Facsimile under 37 C.F.R. §1.8; and a fee statement calculating any fee(s) presently due in connection with the filing of this paper, along with an authorization to charge any fee deficiency to Deposit Account No. 23-1925.